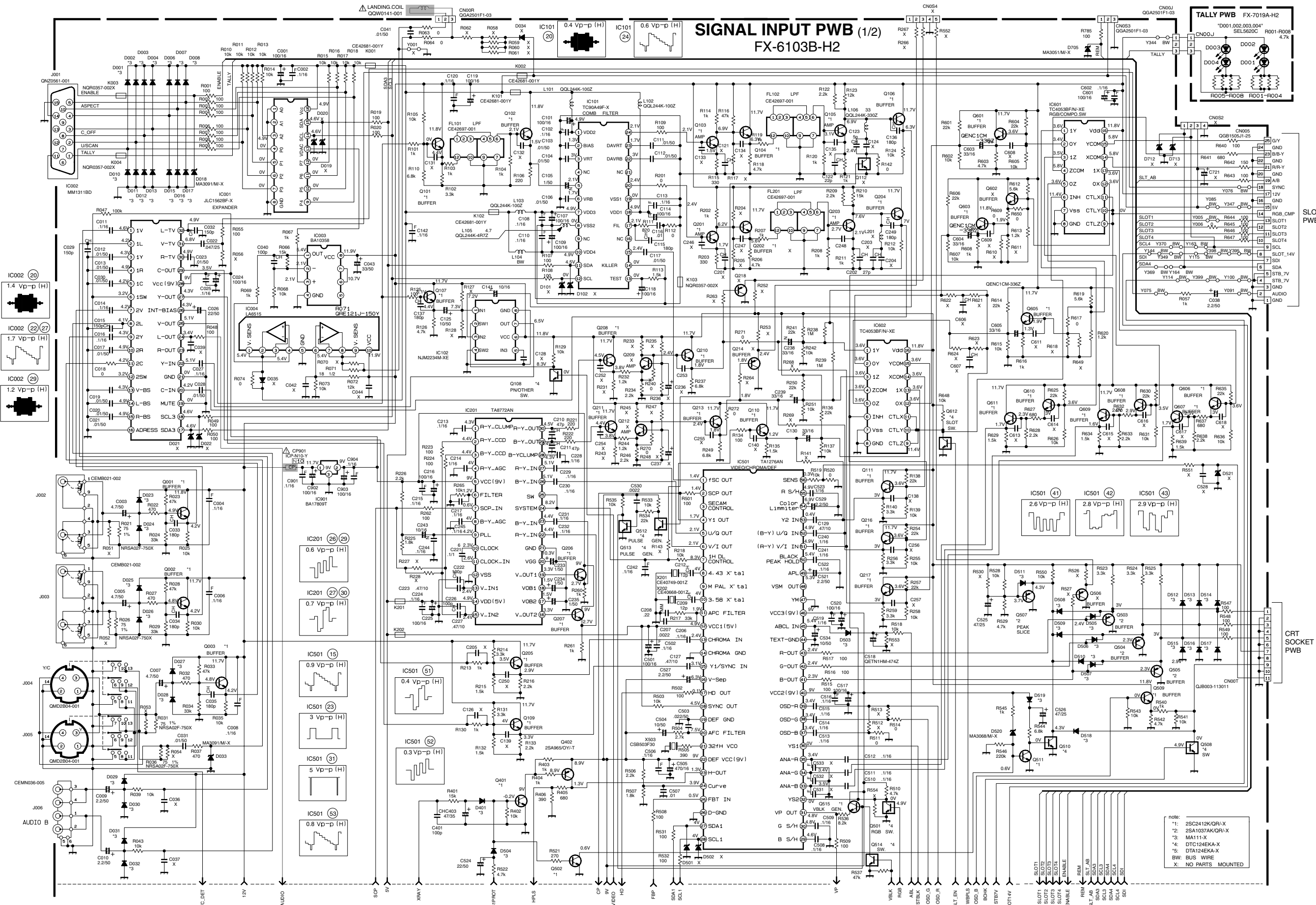
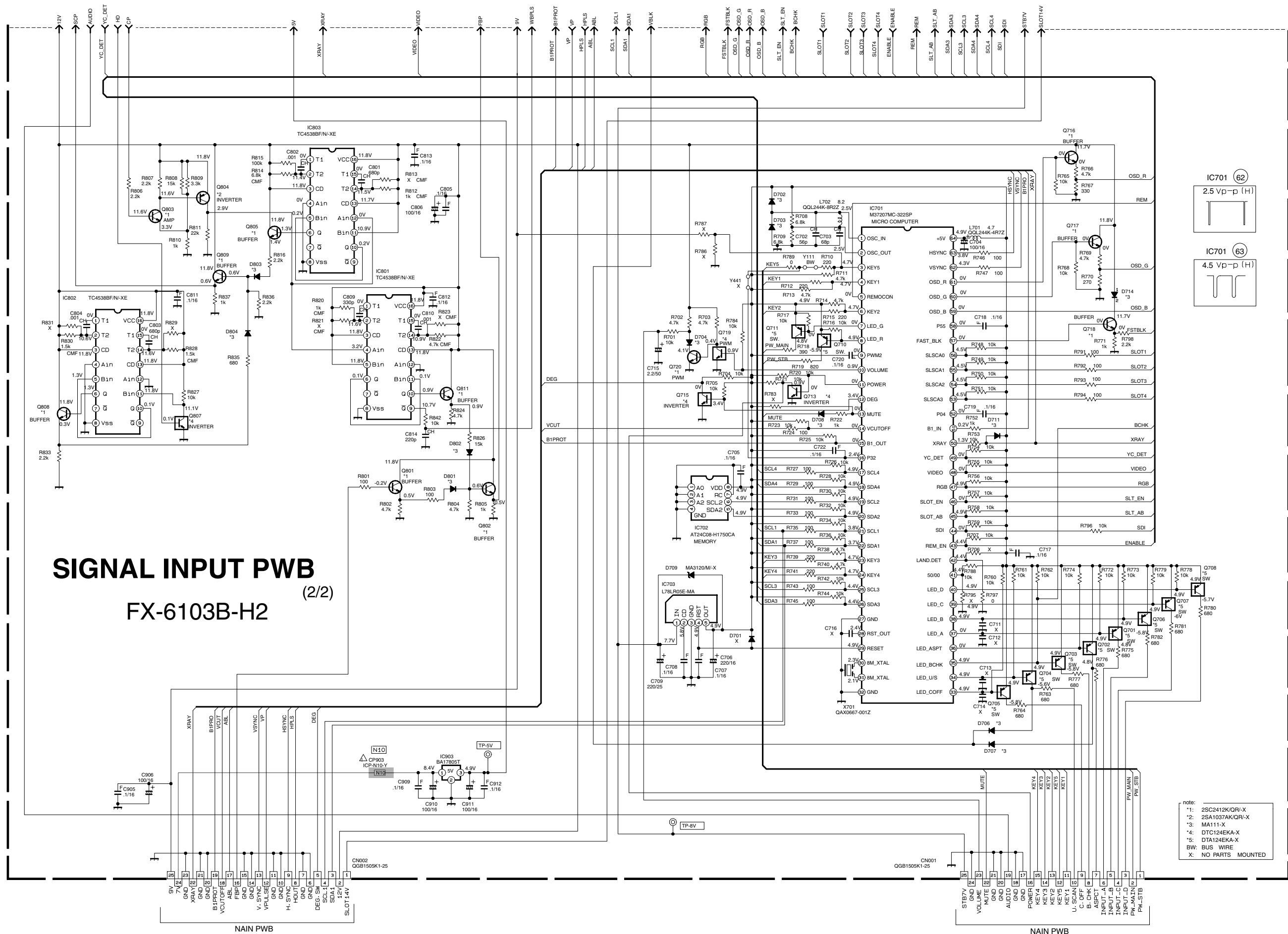


CIRCUIT DIAGRAMS SIGNAL INPUT & TALLY PWB CIRCUIT DIAGRAMS [1/2]

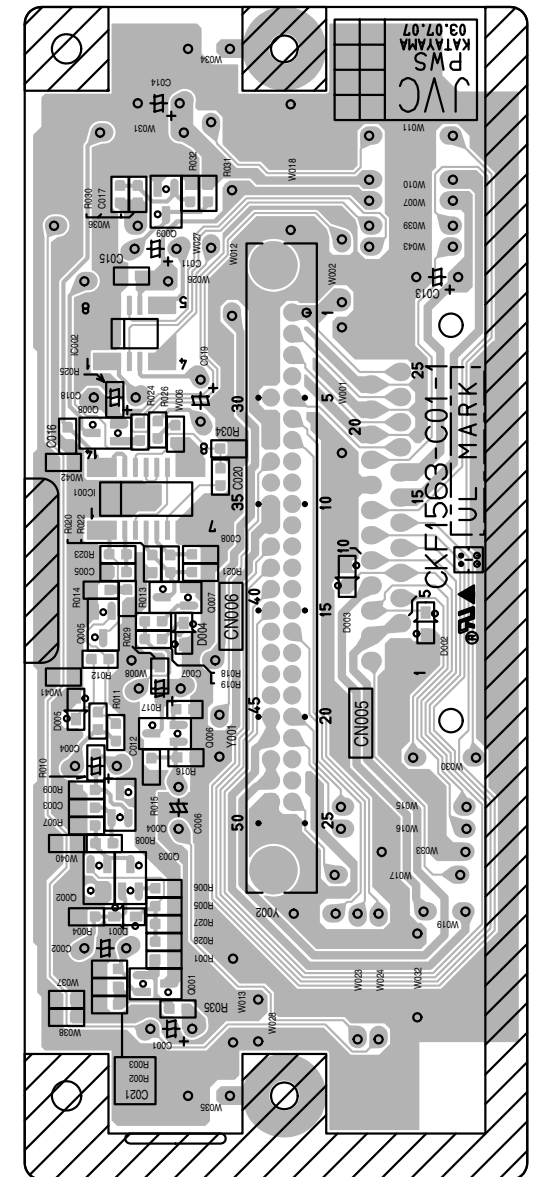
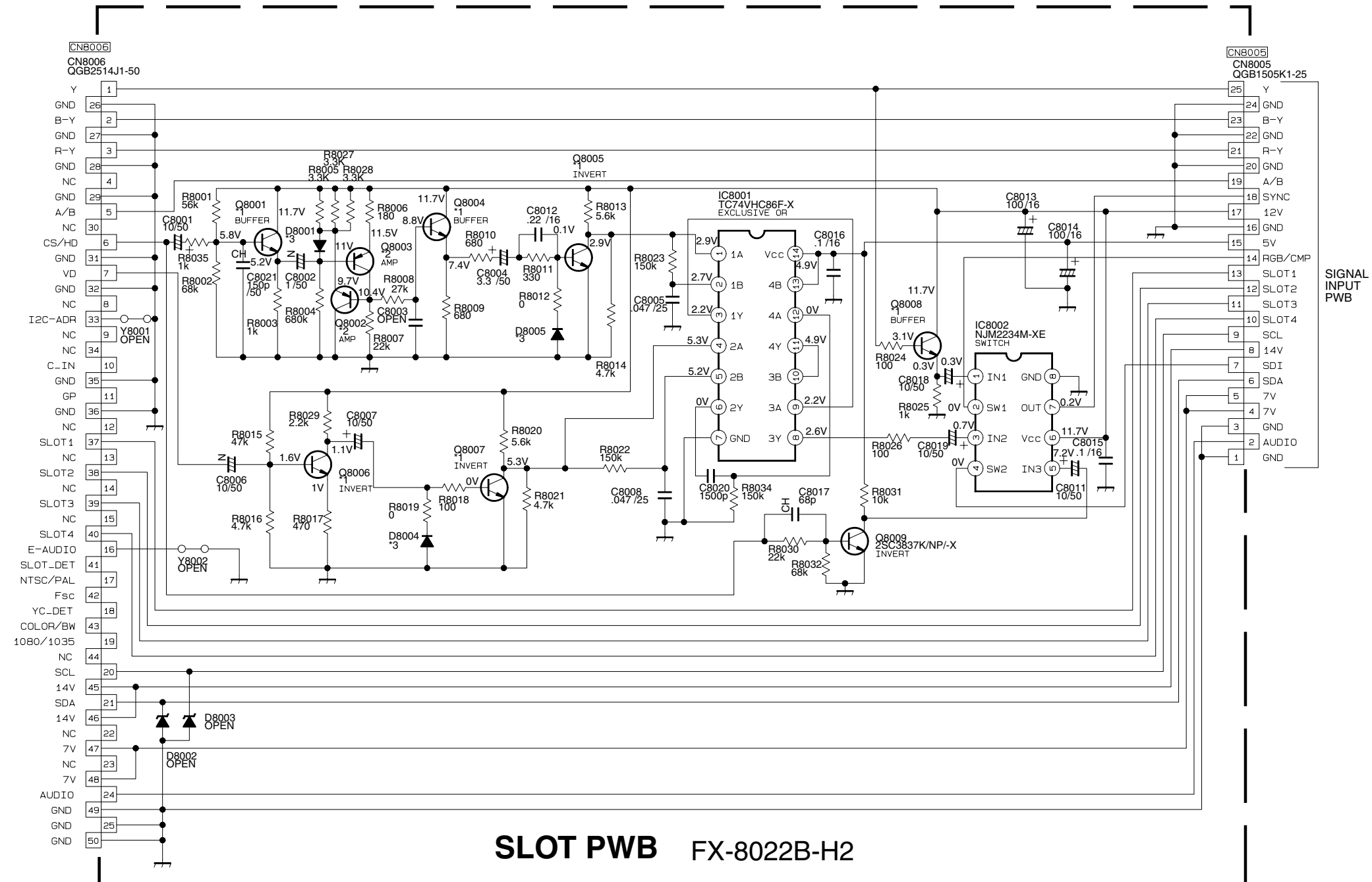


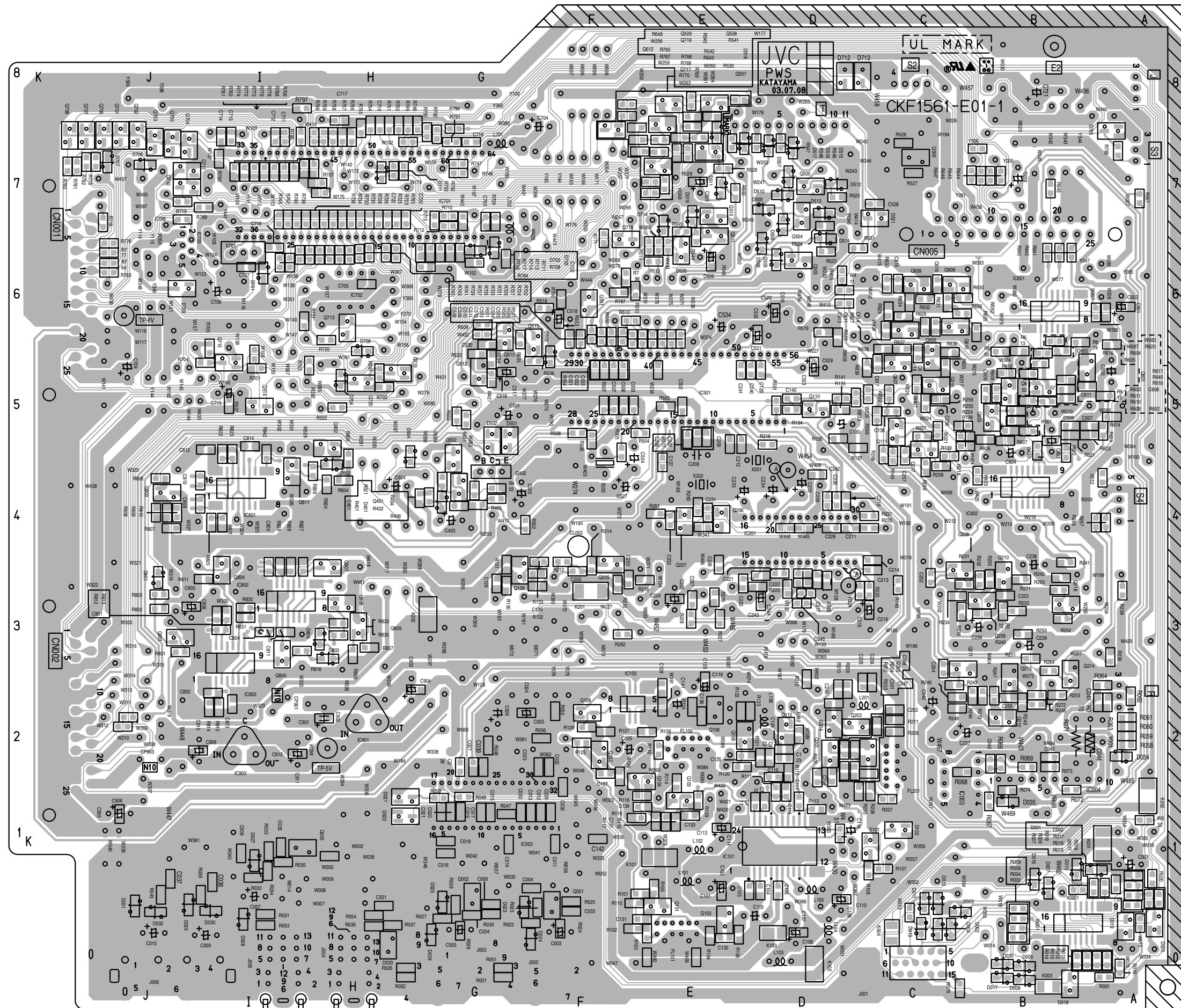
SIGNAL INPUT PWB CIRCUIT DIAGRAM [2/2]



SLOT PWB CIRCUIT DIAGRAM

PATTERN DIAGRAMS *SLOT PWB PATTERN*





 **TOP**